Docket No.: 4590-497

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE APR 2006

:

In re Application of

Richard MORISSON

Confirmation No.

U.S. Patent Application No. not yet assigned

Filed: herewith

For:

COMPARISON CICUIT FOR ANALOG/DIGITAL CONVERTER

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above-identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. A copy of the foreign search report is attached for the Examiner's information. Acknowledgement and consideration of these documents are respectfully requested.

Respectfully submitted,

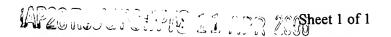
LOWE HAUPTMAN & BERNER, LLP

Genneth My Berner

Kenneth M. Berner Registration No. 37,093

1700 Diagonal Road, Suite 310 Alexandria, Virginia 22314 (703) 684-1111 KMB/iyr Facsimile: (703) 518-5499

Date: April 11, 2006



INFORMATION DISCLOSURE CITATION IN AN APPLICATION

ATTY. DOCKET NO.
4590-497

APPLICANT
Richard MORISSON

FILING DATE
herewith

GROUP

| (PTO-1449) | | | herewith | herewith | | | |
|------------------------|--|---|--|---------------------------------|--|-------------------------------------|--|
| | | | TENT DOCUMENTS | | - '- '- '- '- '- '- '- '- '- '- '- '- '- | 1 | |
| EXAMINER'S INITIALS | PATENT NO. DATE | | NAME | CLASS | SUBCLASS | FILING DATE | |
| | 6,169,510 | 2001-01-02 | Bult Klaas et al | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | - | | | | | |
| | | | | | | | |
| | | + | | | | | |
| | | | | | | | |
| ⊕ €°3 °- | | FOREI | GN PATENT DOCUMEN | TS | far i v | | |
| EXAMINER'S INITIALS | PATENT NO. | DATE | COUNTRY | CLASS | SUBCLASS | Translation Yes No | |
| INITIALS | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | · | | | | | |
| | | | 20th 10 cd. 100 | | FA: N | 2 1 20 | |
| | The same of the sa | C. and and | ng Author, Title, Date, Per | | | | |
| | Choi M et al "A Circuits, Decemb | 6b 1.3GSample/s A/l per 2001, pages 1847 | D converter in 0.35/spl mu -1858 | ı/m CMOS" II | EEE Journal o | 1 Solid State | |
| | page 1852, cologi | ne 2, ligne 8 | 2, ligne 27- page 1848, colo | | | | |
| | Leuciuc A et al: and Systems, vol 1-page 394, color | . 2, May 26, 2002-M | ring for A/D Converters" ay 29, 2002 pages II 392-I | IEEE Internat I 395, figures | tional Symposi 1, 2, 9 page 393 | um on Circuits 3, colonne 2, ligne | |
| EXAMINER | | 1, ng 02 | DATE CONSIDE | DATE CONSIDERED | | | |

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.